

Electronic Acknowledgement Receipt

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Application Number:	10755042
International Application Number:	
Confirmation Number:	8665
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same
First Named Inventor/Applicant Name:	Mou-Shiung Lin
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Transmittal Letter	Transmittal.pdf	66241	no	1
			b90ab9234378e8d9d704977dd8f61325e80cc450		

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2	Request for Continued Examination (RCE)	RCE.pdf	93957	no	1
			6e335c034fc1ce35a8be1fcd53a4130cac0a668		

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3		ResponsetoFinalOA.pdf	122627	yes	14
			c009c7fea285375bd1bd2a5ae2f4c9f0d7f528ea		

Multipart Description/PDF files in .zip description

	Document Description	Start	End
	Amendment After Final	1	1
	Claims	2	8
	Applicant Arguments/Remarks Made in an Amendment	9	14

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4	Miscellaneous Incoming Letter	Appendix.pdf	48189	no	1
			221b8a9de601147508faa30915e13aaf9051c509		

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5	Transmittal Letter	IDS-Transmittal.pdf	65153	no	2
			68b03d148e824491c4995a99c00eec8009c56fda		

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6	Information Disclosure Statement (IDS) Form (SB08)	IDS-1449.pdf	85022	no	1
			7eb57d5dbd1406c9ab360507a1774be90e6368b7		

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7	Transmittal Letter	MG-0104-062711-IDS-Transmittal.pdf	65620	no	2
			6aa3fd5f8e77c57f4e05975e7a9d21422cc2d189		
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8	Information Disclosure Statement (IDS) Form (SB08)	MG-0104-062711-IDS-1449.pdf	106445	no	3
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10	Non Patent Literature	KUO_Semiconductor_Packaging_Engineering.pdf	448133	no	3
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Information:					
11	Fee Worksheet (SB06)	fee-info.pdf	29976	no	2
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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